

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

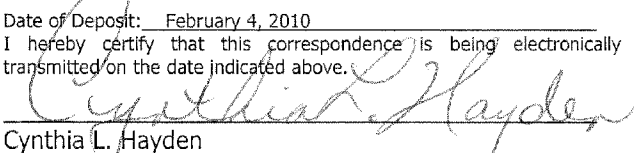
In re Applicant:	Hong Jiang et al.	§	Art Unit:	2195
Serial No.:	10/750,583	§	Examiner:	Eric Charles Wai
Filed:	December 31, 2003	§	Conf. No.:	8582
For:	Processing Architecture Having Passive Threads and Active Semaphores	§	Docket:	ITL.1704US P17510
		§	Assignee:	Intel Corporation

Mail Stop **Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO OFFICE ACTION

Sir:

In response to the office action mailed November 24, 2009, please amend the above-referenced patent application as follows:

Date of Deposit: February 4, 2010
I hereby certify that this correspondence is being electronically
transmitted on the date indicated above.

Cynthia L. Hayden